

Conference Program



5th European Conference on Circuits and Systems for Communications (ECCSC'10)

November 23–25, 2010, Belgrade, Serbia

Website: <http://eccsc10.etf.rs>

Correspondence: eccsc10@etf.rs

Venue: Sava Center, Milentija Popovića 9, Belgrade, Serbia

Organized by the School of Electrical Engineering, University of Belgrade,
and the Telecommunications Society, Belgrade, Serbia

Technical co-sponsorship:

IEEE Circuits and Systems Society

IEEE Serbia and Montenegro Section

IEEE Serbia and Montenegro Communications Chapter

IEEE Serbia and Montenegro joint Signal Processing and Circuits and
Systems Chapter

IEEE Serbia and Montenegro Education Chapter

About ECCSC'10

After the success of ICCSC'02 (St. Petersburg, Russia), ICCSC'04 (Moscow, Russia), ICCSC'06 (Bucharest, Romania), and ECCSC'08 (Bucharest, Romania), the present ECCSC'10 conference is organized by the School of Electrical Engineering, University of Belgrade, and the Telecommunications Society, Belgrade, Serbia.

ECCSC'10 is organized in parallel with the Telecommunications Forum TELFOR 2010, the greatest regional event in the fields of Telecommunications and Information Technologies, with TELFOR being the host. Both conferences are co-organized and co-supported by IEEE. Events are being organized in the SAVA conference center, with the joint on site management, joint Opening Ceremony, welcome party, exhibition and interesting technical and commercial presentations organized within TELFOR. More than 1000 participants from both conferences are expected to attend the Opening Ceremony and all of them are welcome as highly esteemed guests.

Conference Committees

Honorary Chair

Rui J. P. de Figueiredo, University of California, Irvine, USA

Conference Co-Chairs

Dejan V. Tošić, University of Belgrade, Serbia

Đorđe Paunović, University of Belgrade, Serbia

Florin Constantinescu, Politehnica University, Bucharest, Romania

Conference Advisor

Ljiljana Trajković, Simon Fraser University, Canada

Technical Program Co-Chairs

George S. Moschytz, Bar-Ilan University, Israel

Ljiljana Milić, University of Belgrade, Serbia

Branimir Reljin, University of Belgrade, Serbia

Miroslav Lutovac, University of Belgrade, Serbia

Technical Program Committee

Magdy Bayoumi, University of Louisiana, Lafayette, USA

Jean Baptiste Bequeret, ENSEIER, Bordeaux, France

Silviu Ciochina, Politehnica University, Bucharest, Romania

Đurađ Budimir, University of Westminster, London, UK

Dragoš Cvetković, Serbian Academy of Sciences and Arts, Serbia

Anthony C. Davies, Kingston University, London, UK

Yann Deval, ENSEIER, Bordeaux, France

Gordana Jovanović Doleček, INAOE, Puebla, Mexico

Miroslav Dukić, University of Belgrade, Serbia

Liviu Goras, Facultatea de Electronica si Telecomunicatii, Iasi, Romania

Mihai Iordache, Politehnica University, Bucharest, Romania

Andreas Kaiser, Universite Catholique de Lille, groupe ISEN, France

Dragan Kandić, University of Belgrade, Serbia

Vladimir Katić, University of Novi Sad, Serbia

Eric Kerherve, ENSEIER, Bordeaux, France

Branko Kolundžija, University of Belgrade, Serbia

Branko Kovačević, University of Belgrade, Serbia

Alexander S. Korotkov, St. Petersburg State Technical University, St. Petersburg, Russia

Juha Kostamovaara, University of Oulu, Finland
Stanciu Lucian, Politehnica University, Bucharest, Romania
Budimir Lutovac, University of Montenegro, Montenegro
Wolfgang Mathis, University of Hannover, Germany
Ion Marghescu, Politehnica University, Bucharest, Romania
Bratislav Milovanović, University of Niš, Serbia
Veljko Milutinović, University of Belgrade, Serbia
Cristian Negrescu, Politehnica University, Bucharest, Romania
Predrag Pejović, University of Belgrade, Serbia
Vladimir Petrović, University of Belgrade, Serbia
Miodrag Popović, University of Belgrade, Serbia
Jovan Radunović, RATEL, Serbia
Slobodan K. Simić, Mathematical Institute of the Serbian Academy of Sciences and Arts, Serbia
Stanislaw Szczepanski, Gdansk University of Technology, Poland
Jurij Tasić, University of Ljubljana, Slovenia
Slobodan Vukosavić, University of Belgrade, Serbia

Local Organizing Committee Co-Chairs

Milan Ilić, University of Belgrade, Serbia
Aleksandar Nešković, University of Belgrade, Serbia
Nataša Nešković, University of Belgrade, Serbia

Local Organizing Committee

Jelena Čertić, University of Belgrade, Serbia
Nenad Krajnović, University of Belgrade, Serbia
Dragan Olćan, University of Belgrade, Serbia
Mirjana Simić, University of Belgrade, Serbia
Slobodan Savić, University of Belgrade, Serbia

Publications

Miroslav Lutovac, University of Belgrade, Serbia
Jelena Čertić, University of Belgrade, Serbia

Time Table

23. November 2010 Tuesday		24. November 2010 Wednesday		25. November 2010 Thursday
08:30 Registration		CommCir-2 Communication Circuits 8:30–10:45 Room 6/1 (1 st floor)	EmbedSys-1 Embedded systems 8:30–10:45 Room 5/1 (1 st floor)	ModSimAlg-2 Modeling, Simulation, and Algorithms 8:30–10:45 Room 5/1 (1 st floor)
10:30 Opening Ceremony Hall 1				
12:00–13:00 Welcome Party		Plenary-2 11:00–13:15 Japan Salon (1 st floor)		
Plenary-1 14:30–16:45 Japan Salon (1 st floor)		CommCir-3 Communication Circuits 14:30–16:45 Room 6/1 (1 st floor)	EmbedSys-2 Embedded systems 14:30–16:45 Room 5/1 (1 st floor)	
CommSys-1 Communication Systems 17:00–19:15 Room 5/1 (1 st floor)	CommCir-1 Communication Circuits 17:00–19:15 Room 6/1 (1 st floor)	CommCir-4 Communication Circuits 17:00–19:15 Room 6/1 (1 st floor)	ModSimAlg-1 Modeling, Simulation, and Algorithms 17:00–19:15 Room 5/1 (1 st floor)	
Social event		Social event		Social event

Social events depend on the attendees' preferences and weather.

Plenary Sessions

Plenary-1

23rd November 2010

Tuesday 14:30–16:45

Japan Salon (1st floor)

An Overview on Wireline Communication Systems for High-Speed Broadband Communications

Michael Green

Department of EECS, University of California, Irvine, USA

Advances in 3D EM Modeling of Complex Electrically Large Structures

Branko Kolundzija

School of Electrical Engineering, University of Belgrade, Serbia

Plenary-2

24th November 2010

Wednesday 11:00–13:15

Japan Salon (1st floor)

Circuit and Field Models for Power BAW Resonators

Florin Constantinescu, Miruna Nutescu, Alexandru Gheorghe, Mihai Maricaru
Dept. of Electrical Engineering, Politehnica University, Bucharest, Romania

Complex Networks, Spectral Analysis, and Internet Topologies

Ljiljana Trajković

School of Engineering Science, Simon Fraser University, Vancouver, Canada

Causality of Circuit and Electromagnetic-Field Models

Antonije R. Đorđević, Dejan V. Tošić

School of Electrical Engineering, University of Belgrade, Serbia

Regular Sessions

Note to presenters

Each paper is presented orally (10 minutes) followed by a short discussion (5 minutes).

Authors can prepare slides, for example, in Microsoft PowerPoint (ppt) or PDF formats.

It is a good practice to have an alternative presentation in PDF with all fonts embedded. This way the presentation can be shown as expected on practically all systems.

CommSys-1
Communication Systems
23rd November 2010
Tuesday 17:00–19:15
Room 5/1 (1st floor)

Chair Persons: Z. Stamenković, Milan Bjelica

Base Station Cooperation in Multi-Cellular System Using Multi-Stream Communications

Tetsuki Taniguchi, Yoshio Karasawa, Nobuo Nakajima

MAC and Baseband Hardware Platforms for RF-MIMO WLAN

Z. Stamenković, K. Tittelbach-Helmrich, M. Krstić, J. Ibanez, V. Elvira, I. Santamaria
(Invited Paper)

Effective Minimization of GTP Tunnels for MBMS Data Transfer in UMTS

Rama Mohan Babu K. N., Mahesh Kumar B. M., K. N. Balasubramanya Murthy, Mamatha

Towards Ubiquitous Distributed Multimedia: UPnP-Based Control and Scripting

Milan Z. Bjelica, Bojan Mrazovac, Nikola Teslić

Analysis of the Service Quality on MPLS Networks

Özge Güre, Burak Kaan Boyaci, N. Özlem Ünverdi

Performance Analysis of Registration Procedure in IEEE 802.3av Networks

Milan Bjelica

Distributed Change Detection Based on a Randomized Consensus Algorithm

Srdjan S. Stanković, Nemanja Ilić, Miloš S. Stanković, Karl Henrik Johansson

CommCir-1
Communication Circuits
23rd November 2010
Tuesday 17:00–19:15
Room 6/1 (1st floor)

Chair Person: M. F. Hribšek

Elimination of Analog Switches in Low-Bit Successive Approximation ADCs

Masumeh Damghanian, Hossein Shamsi

PSO Optimized Concurrent Dual-Band LNA with RF Switch for Better Inter-Band Isolation

Kunal Datta, Rohit Datta, Ashudeb Dutta, T. K. Bhattacharyya

Topology Oriented Symbolic Circuit Analysis Based on Limit Variables

Srđan Đorđević, Predrag Petković

UWB Microwave Chaotic Oscillator: From Distributed Structure to CMOS IC Realization

Elena V. Efremova, Artem Yu. Nikishov, Andrey I. Panas

Wide-Bandwidth 60 GHz Differential LNA in SiGe:C Technology

Dušan Grujić, Milan Savić, Can Bingöl, Lazar Saranovac

Multilayer Substrate Integrated Waveguide Bandpass Filters

Oleksandr Glubokov, Milka M. Potrebić, Đurađ Budimir

A Low Power CMOS LNA for 2.45-GHz Band IEEE 802.15.4 Standard in 0.18- μm Technology

Hosein Alavi-Rad, Goodarz Cheraghi, Soheil Ziabakhsh

Design and Realization of Transversal Surface Acoustic Wave RF Filters

Marija F. Hribšek, Dejan V. Tošić, Miodrag Tasić, Zoran Filipović, Zdravko Živković

Measurement and Modelling of 1/f Noise in 180 nm NMOS and PMOS Devices

Nikos Mavredakis, Angelos Antonopoulos, Matthias Bucher

CommCir-2
Communication Circuits
24th November 2010
Wednesday 8:30–10:45
Room 6/1 (1st floor)

Chair Person: A. R. Đorđević

A Novel High Gain Two Stage Ultra-Wide Band CMOS LNA in 0.18 μm Technology

Ehsan Kargaran, Hojat Khosrowjerdi, Karim Ghaffarzadegan, Moosa Kenarroodi

Design of 0.5 V, 450 μW CMOS LNA Using Current Reuse and Forward Body Bias Technique

Ehsan Kargaran, Mehdi Mohammad Kazemi

Bandgap Based Voltage Reference Circuit With Higher Order Compensation

Siddharth Katare

Resistorless Low Power Voltage Reference Circuit

Siddharth Katare

A Sub-1V High-Gain Two-Stage OTA Using Bulk-Driven and Positive Feedback Techniques

H. Khameh, H. Shamsi

Hybrid CT/DT Resonance-Based Cascade $\Sigma\Delta$ Modulators for Broadband Low-Voltage Applications

Mohammad Hossein Maghami, Mohammad Yavari

Asynchronous Sigma-Delta Analog-to-Digital Converter

Tomislav Matić, Tomislav Švedek, Davor Vinko

Teaching Fundamentals of Electrical Engineering: Nodal Analysis

Antonije R. Đorđević, Gradimir N. Božilović, Dragan I. Olćan

CommCir-3
Communication Circuits
24th November 2010
Wednesday 14:30–16:45
Room 6/1 (1st floor)

Chair Person: P. Pejović

An Analysis of Three-Phase Rectifiers with Constant Voltage Loads

Predrag Pejović, Johann W. Kolar (Invited Paper)

A Novel Digital Background Calibration Technique for Pipelined ADCs

Tohid Moosazadeh, Mohammad Yavari

Improve the Automatic Clock Gating Insertion in ASIC Synthesis Process using Optimal Enable Function Selection

Miloš Nikolić, Mihajlo Katona

Perspective High-Frequency Correction in Differential and Broadband Amplifiers

N. N. Prokopenko, A. I. Serebryakov, P. S. Budyakov

Analog Controlled Amplifiers and Voltage Multipliers Based on Modified Gilbert Cells

N. N. Prokopenko, P. S. Budyakov, A. I. Serebryakov

New Principle of the Phase-Locked Loop Operation

V. Rapin

An On-Chip Low Power Low Phase Noise VCO with Frequency Divider for Low Jitter Low Phase Noise Applications

Kriyang Shah, Hai P. Le, Jugdutt Singh, Yashodhan Moghe, Andrew Brawley

CommCir-4
Communication Circuits
24th November 2010
Wednesday 17:00–19:15
Room 6/1 (1st floor)

Chair Person: R. Mc Sweeney

On Resource Effective High-Throughput Reed-Solomon Decoders

Richard Mc Sweeney, Christian Spagnol, Emanuel Popovici

Effects of Mounting Pads and Length Imbalance on Performance of Fast Differential Interconnects

Mirjana Ž. Stojilović, Nikola M. Nenadić, Antonije R. Đorđević

Designing Asymmetric 2.2 GHz RF Oscillator by Design of Experiments Using Taguchi Methods

Jai Narayan Tripathi, Jayanta Mukherjee, Prakash R. Apte

Design of Micro-Power Amplifier in Neural Recording Application with Improved Noise Efficiency Factor

Parisa Vejdani, Fatemeh Sadat Saadati

Rectifier and Modulator Architecture in Passive RFID Transponders

Davor Vinko, Tomislav Švedek, Drago Žagar

Modified Nodal Analysis Formulation of Operational Transconductance Amplifier

Oguzhan Demiryurek, Ali Bekir Yildiz

An Analysis of Voltage-Driven Passive-Mixer Based SAW-Less Transmitters

Hossein Zarei

A CMOS Down-Conversion Mixer with High Linearity and Low Noise Figure in 0.18- μm Technology

Soheil Ziabakhsh, Goodarz Cheraghi, Hosein Alavi-Rad

EmbedSys-1
Embedded systems
24th November 2010
Wednesday 8:30–10:45
Room 5/1 (1st floor)

Chair Person: M. Oltean

VHDL Modeling of PHY and MAC Layer Modules for Underwater Optical Wireless Communication

Davide Anguita, Davide Brizzolara, Giancarlo Parodi

Design of FIR Pulse-Shaping Filter: Superiority of Differential Evolution Optimization over Convex Optimization

S. Chattopadhyay, S. K. Sanyal, A. Chandra

JPEG XR Encoder Implementation on a Heterogeneous Multiprocessor System

Dragomir El Mezeni, Aleksandar Berić, Edwin van Dalen, Lazar Saranovac

An Emerging Architecture and Implementation for UWB MAC Validation

Sathish Kumar Ganesan, Prashant Dixit, Manish Kumar Saxena, Ekansh Bhatnagar

Architecture and Implementation of Modular Wireless Sensor Network Node

Vladimir B. Kovačević, Darko Živanović, Marko Nikolić, Željko Stojković

A MATLAB Toolbox Synthesizing Reconfigurable Delta-Sigma Modulators for Multi-Standard Wireless Applications

B. Hoda Seyedhosseinzadeh, Mohammad Yavari

High Speed Near-ML MIMO Detection for the Next Generation Wireless Systems

Bojan D. Marić, Miloš S. Trajković, Lazar V. Saranovac

BER per Scale Performance of a Wavelet OFDM Transmission through Time and Frequency Selective Channels

Marius Oltean, Ioana Firoiu

EmbedSys-2
Embedded systems
24th November 2010
Wednesday 14:30–16:45
Room 5/1 (1st floor)

Chair Person: K. Takei

**Computational Design of Wireless Interface for RFID Reader Module
Applied to Mobile Devices**

Ken Takei

**Development of Modern Embedded Electronics (PCB) for a Small-
Volume Production**

Danko Miočinović , Ivan Rešetar, Mihajlo Katona

Blind MIMO Equalization with Tagging Filter Based on All-Pass Filter

Naoto Sasaoka, James Okello, Tatsuya Nishimoto, Yoshio Itoh

**Fast Video Decoder for Low-Cost Embedded CPUs: A Software
Solution and a Case Study**

Bojan Mrazovac, Milan Z. Bjelica, Nikola Teslić

**An Automated Hardware Testing Using PXI Hardware and LabVIEW
Software**

Milan D. Nešković, Jelena S. Spasić, Vladimir V. Čelebić, Iva M. Salom

**Design of IEEE 1451 Smart Transducer Communication Module for
Embedded Application**

Ivan T. Popović, Nikola M. Rajović

**Multi-Binary Turbo-Code Design Based on Convergence of Iterative
Turbo-Decoding Process**

H. Balta, M. Kovaci, M. Nafornita, M. Balta

Implementation of the Reliable Network Links on the FPGA Chips

Vukota Šoškić, Zoran Čiča, Aleksandra Smiljanić

ModSimAlg-1
Modeling, Simulation, and Algorithms
24th November 2010
Wednesday 17:00–19:15
Room 5/1 (1st floor)

Chair Persons: M. S. Lee, V. Petrović

A study on the Enhancement of Gain and Axial Ratio Bandwidth of the Multilayer CP-DRA

Ho Sang Lee, Mun Soo Lee

Strong FEM Formulation for Quasi-Static Analysis of Shielded Planar Transmission Lines in Anisotropic Media

Vladimir V. Petrović, Žaklina J. Mančić

Analysis of Coupled Oscillators Applied to 1D Antenna Array

Mihai Iordache, Lucia Dumitriu, Iulia Dumitrescu, Mihaela Ionita

Level Crossing Rate of MRC with Transmit Antenna Selection in Unequally Distributed Nakagami Fading Channels

Vesna Blagojević, Predrag Ivaniš

A model of rain attenuation in Ka band based on the Wiener prediction

Da Xinyu, Shen Yong, Wang Yanling

The Effect of Varying Cyclic Prefix on Residual Constellation Error in OFDM Technology Using a Novel Simulink-VSA Based WiMAX Transceiver

B. Bhattacharyya, I. Saha Misra, S. K. Sanyal

Tool to Simulate Groups in Vehicular Networks Using NS-2 and TraceGraph

C. Caballero-Gil, P. Caballero-Gil, J. Molina-Gil

An Asymptotic Statistical Analysis of the Hyperanalytic Wavelet Transform

Ioana Firoiu, Corina Nafornta, Dorina Isar, Jean-Marc Boucher, Alexandru Isar

ModSimAlg-2
Modeling, Simulation, and Algorithms
25th November 2010
Thursday 8:30–10:45
Room 5/1 (1st floor)

Chair Person: M. D. Lutovac

Simulation of OFDM Transmitters and Post Processing with SchematicSolver and Mathematica as a Computer Algebra System

Vladimir M. Mladenović, Dana Porrat, Miroslav D. Lutovac

Markov Channel Model for MIMO Systems and its Applications for Efficient Error Control

Milena M. Stojnić, Predrag N. Ivaniš

Negative Group Delay and Causality

Antonije R. Đorđević, Dejan V. Tošić

Optimized Lagrange and Spline-Based Interpolators

Đorđe Babić

Causal Video Transmission Over Fading Channels with Full Channel State Information

Slavche Pejoski, Venceslav Kafedziski

One-Step Signal Selective Direct Positioning Algorithm of Cyclostationary Signals

Ivan P. Pokrajac, Desimir Vučić

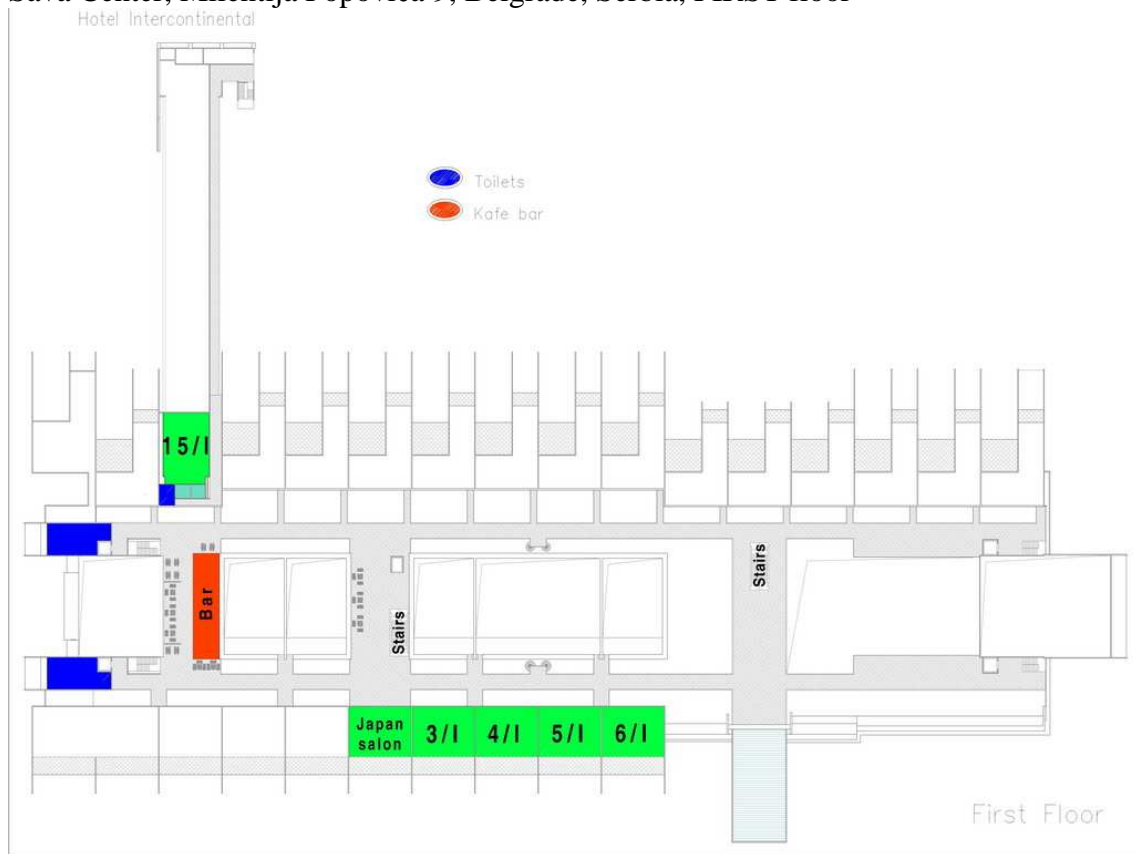
Modeling of Optical Gain in Quantum Cascade Laser Subjected to Strong Magnetic Field

Jelena Radovanović, Dragan Timotijević, Sabina Ramović, Vitomir Milanović

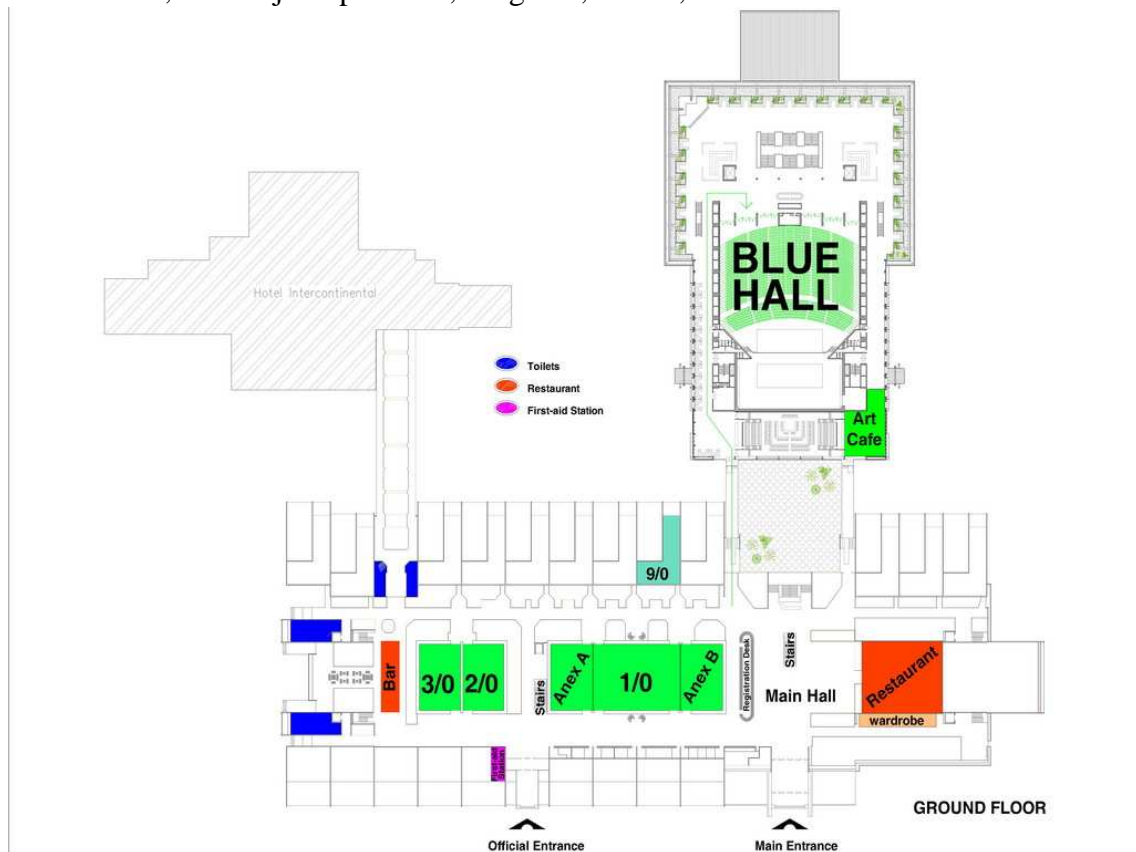
Euler-Bernoulli Equation Two and a Half Centuries Later

Mirjana Filipović

Sava Center, Milentija Popovića 9, Belgrade, Serbia, FIRST floor



Sava Center, Milentija Popovića 9, Belgrade, Serbia, Ground floor



List of Authors Sorted by Surname

Hosein Alavi-Rad
Davide Anguita
Angelos Antonopoulos
Prakash R. Apte

Dorđe Babić
Rama Mohan Babu K. N.
H. Balta
M. Balta
Aleksandar Berić
Ekansh Bhatnagar
B. Bhattacharyya
T. K. Bhattacharyya
Can Bingöl
Milan Bjelica
Milan Z. Bjelica
Vesna Blagojević
Jean-Marc Boucher
Burak Kaan Boyaci
Gradimir N. Božilović
Andrew Brawley
Davide Brizzolara
Matthias Bucher
Đurađ Budimir
P. S. Budyakov

C. Caballero-Gil
P. Caballero-Gil
A. Chandra
S. Chattopadhyay
Goodarz Cheraghi
Florin Constantinescu

Vladimir V. Čelebić
Zoran Čiča

Edwin van Dalen
Masumeh Damghanian
Kunal Datta
Rohit Datta
Oguzhan Demiryurek
Prashant Dixit
Iulia Dumitrescu
Lucia Dumitriu

Ashudeb Dutta

Antonije R. Đorđević
Srđan Đorđević

Elena V. Efremova
Dragomir El Mezeni
V. Elvira

Mirjana Filipović
Zoran Filipović
Ioana Firoiu

Sathish Kumar Ganesan
Karim Ghaffarzagdegan
Alexandru Gheorghe
C. Caballero-Gil
P. Caballero-Gil
J. Molina-Gil
Oleksandr Glubokov
Michael Green
Dušan Grujić
Özge Güre

K. Tittelbach-Helmrich
Marija F. Hribšek

J. Ibanez
Nemanja Ilić
Mihaela Ionita
Mihai Iordache
Alexandru Isar
Dorina Isar
Yoshio Itoh
Predrag N. Ivaniš

Karl Henrik Johansson

Venceslav Kafedziski
Yoshio Karasawa
Ehsan Kargaran
Siddharth Katare
Mihajlo Katona
Mehdi Mohammad Kazemi

Moosa Kenarroodi
 H. Khameh
 Hojat Khosrowjerdi
 M. Kovaci
 Vladimir B. Kovačević
 M. Krstić
 Johann W. Kolar
 Branko Kolundzija
 Mahesh Kumar B. M.
 Sathish Kumar Ganesan
 Manish Kumar Saxena

Hai P. Le
 Ho Sang Lee
 Mun Soo Lee
 Miroslav D. Lutovac

Mohammad Hossein Maghami
 Mamatha
 Žaklina J. Mančić
 Mihai Maricaru
 Bojan D. Marić
 Tomislav Matić
 Nikos Mavredakis
 Vitomir Milanović
 Danko Miočinović
 I. Saha Misra
 Vladimir M. Mladenović
 Yashodhan Moghe
 J. Molina-Gil
 Tohid Moosazadeh
 Bojan Mrazovac
 Jayanta Mukherjee
 K. N. Balasubramanya Murthy

Corina Nafornta
 M. Nafornta
 Nobuo Nakajima
 Jai Narayan Tripathi
 Nikola M. Nenadić
 Milan D. Nešković
 Artem Yu. Nikishov
 Marko Nikolić
 Miloš Nikolić
 Tatsuya Nishimoto
 Miruna Nitescu

James Okello

Dragan I. Olćan
 Marius Oltean
 N. Özlem Ünverdi

Andrey I. Panas
 Giancarlo Parodi
 Slavche Pejovski
 Predrag Pejović
 Ivan P. Pokrajac
 Predrag Petković
 Vladimir V. Petrović
 Emanuel Popovici
 Ivan T. Popović
 Dana Porrat
 Milka M. Potrebić
 N. N. Prokopenko

Hosein Alavi-Rad
 Jelena Radovanović
 Nikola M. Rajović
 Sabina Ramović
 V. Rapin
 Ivan Rešetar

Fatemeh Sadat Saadati
 Iva M. Salom
 I. Santamaria
 S. K. Sanyal
 Lazar Saranovac
 Naoto Sasaoka
 Milan Savić
 Manish Kumar Saxena
 A. I. Serebryakov
 B. Hoda Seyedhosseinzadeh
 Kriyang Shah
 Hossein Shamsi
 Jugdutt Singh
 Aleksandra Smiljanić
 Christian Spagnol
 Jelena S. Spasić
 Z. Stamenković
 Miloš S. Stanković
 Srdjan S. Stanković
 Mirjana Ž. Stojilović
 Željko Stojković
 Milena M. Stojnić
 Richard Mc Sweeney

Vukota Šoškić
Tomislav Švedek

Ken Takei
Tetsuki Taniguchi
Miodrag Tasić
Nikola Teslić
Dragan Timotijević
K. Tittelbach-Helmrich
Dejan V. Tošić
Ljiljana Trajković
Miloš S. Trajković
Jai Narayan Tripathi

N. Özlem Ünverdi

Parisa Vejdani

Davor Vinko
Desimir Vučić

Da Xinyu

Wang Yanling
Mohammad Yavari
Ali Bekir Yildiz
Shen Yong

Hossein Zarei
Soheil Ziabakhsh

Drago Žagar
Darko Živanović
Zdravko Živković

Organizers

School of Electrical Engineering, University of Belgrade



Bulevar kralja Aleksandra 73, P.O. Box 3554
11120 Belgrade, Serbia

Phone: +38111.3248464, FAX: +38111.3248681

E-mail: dekanat@etf.rs

Dean: Prof. Dr. Miodrag Popović

Telecommunications Society



Bulevar kralja Aleksandra 73, P.O. Box 3554
11120 Belgrade, Serbia

Phone: +38111.3221419, +38164.6100100

E-mail: g.paunovic@ieee.org

Chair: Prof. Dr. George Paunović